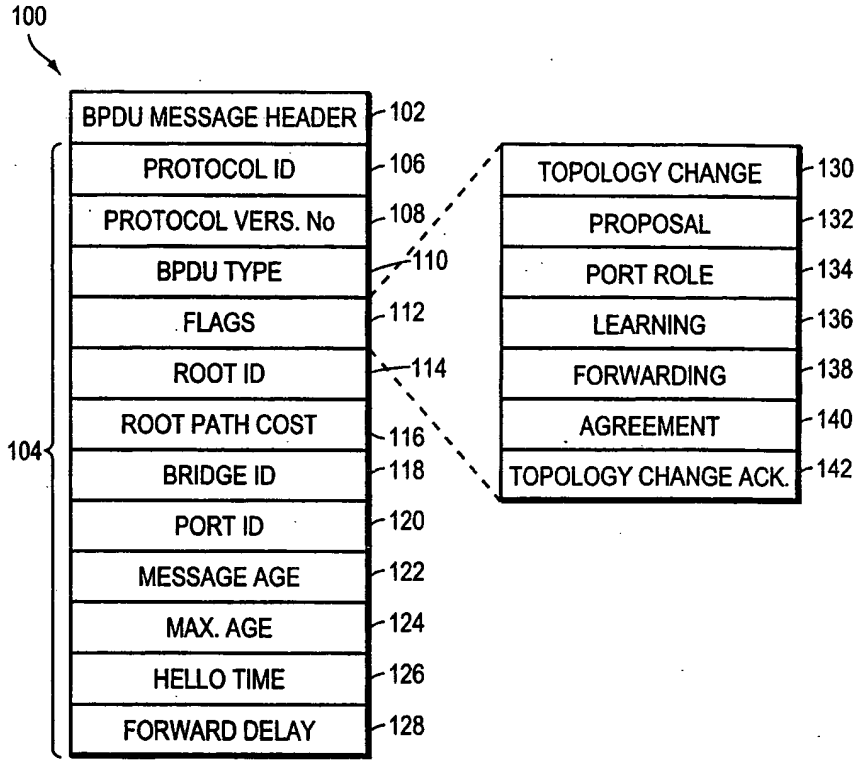


FIG. 1
(PRIOR ART)

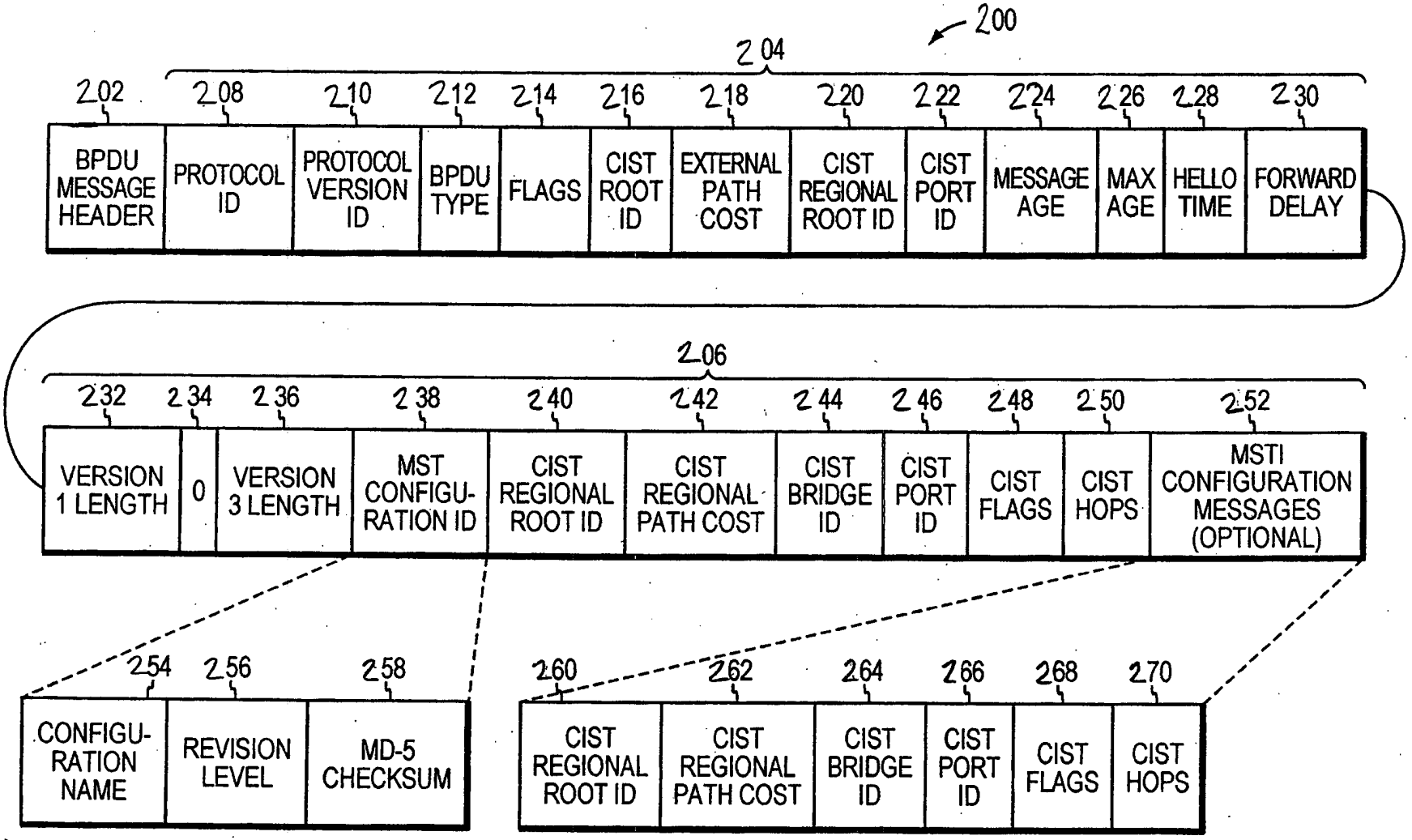


FIG. 2 (PRIOR ART)

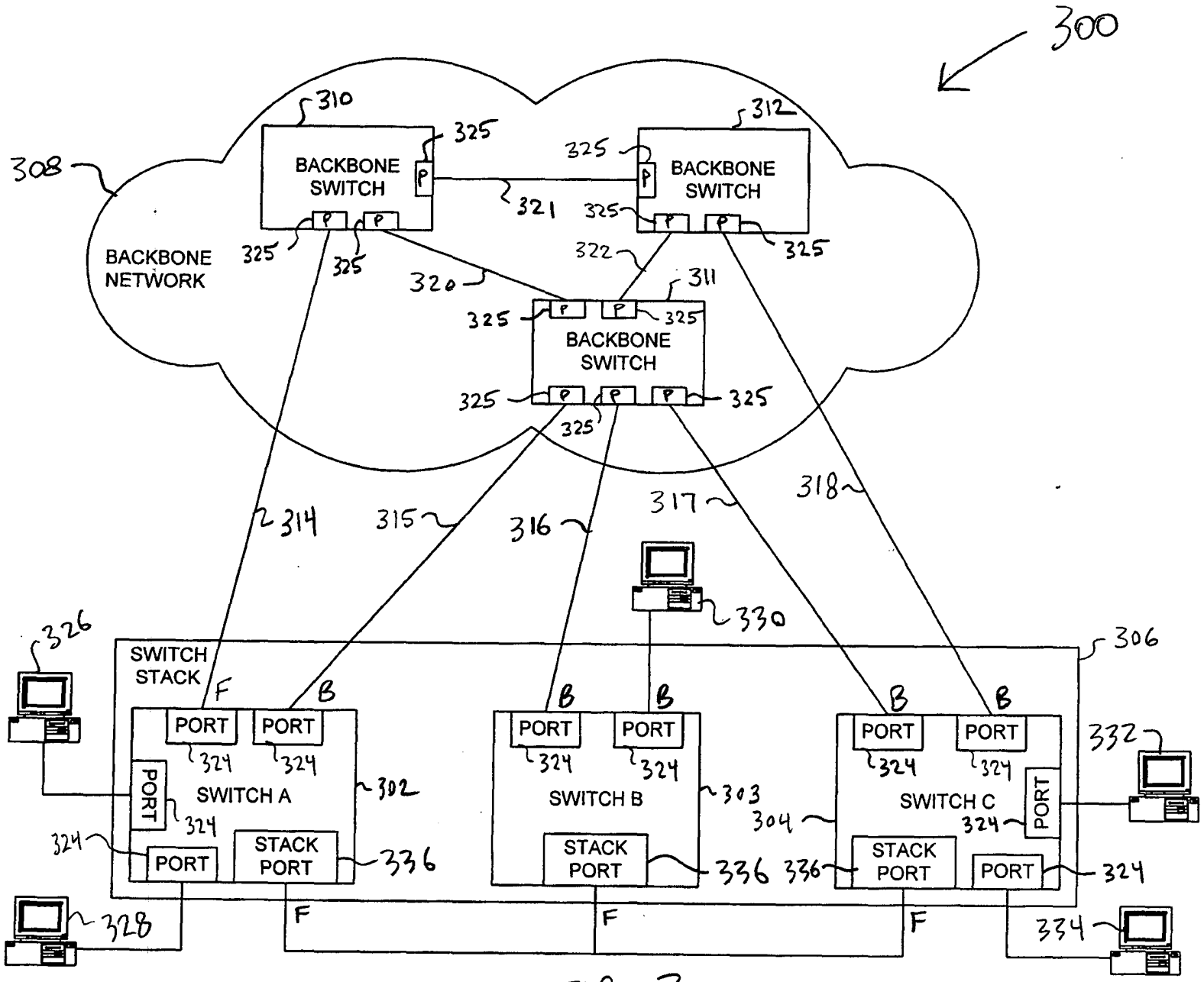
300
310
312
308
BACKBONE SWITCH
BACKBONE SWITCH
BACKBONE NETWORK
321
320
322
311
BACKBONE SWITCH
325
314
315
316
317
318
330
326
306
SWITCH STACK
F
B
PORT
324
SWITCH A
302
SWITCH B
303
SWITCH C
304
STACK PORT
336
328
332
334

FIG. 3

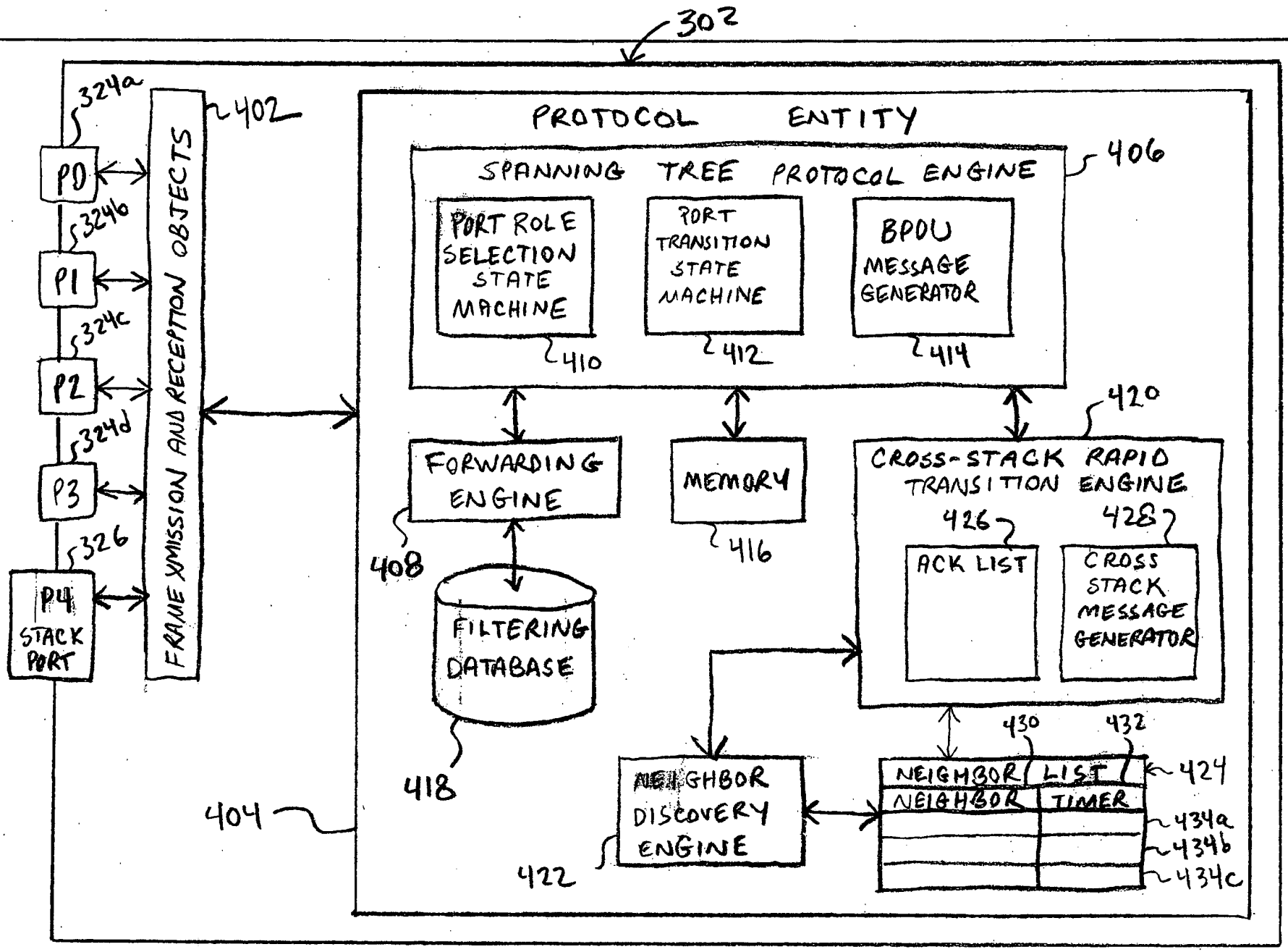


FIG. 4

22-141 50 SHEETS
22-142 100 SHEETS
22-144 200 SHEETS
AMPAD

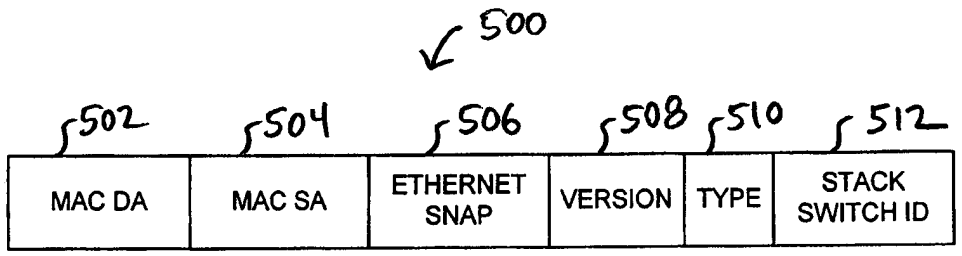
500
502
504
506
508
510
512
MAC DA
MAC SA
ETHERNET SNAP
VERSION
TYPE
STACK SWITCH ID

FIG. 5

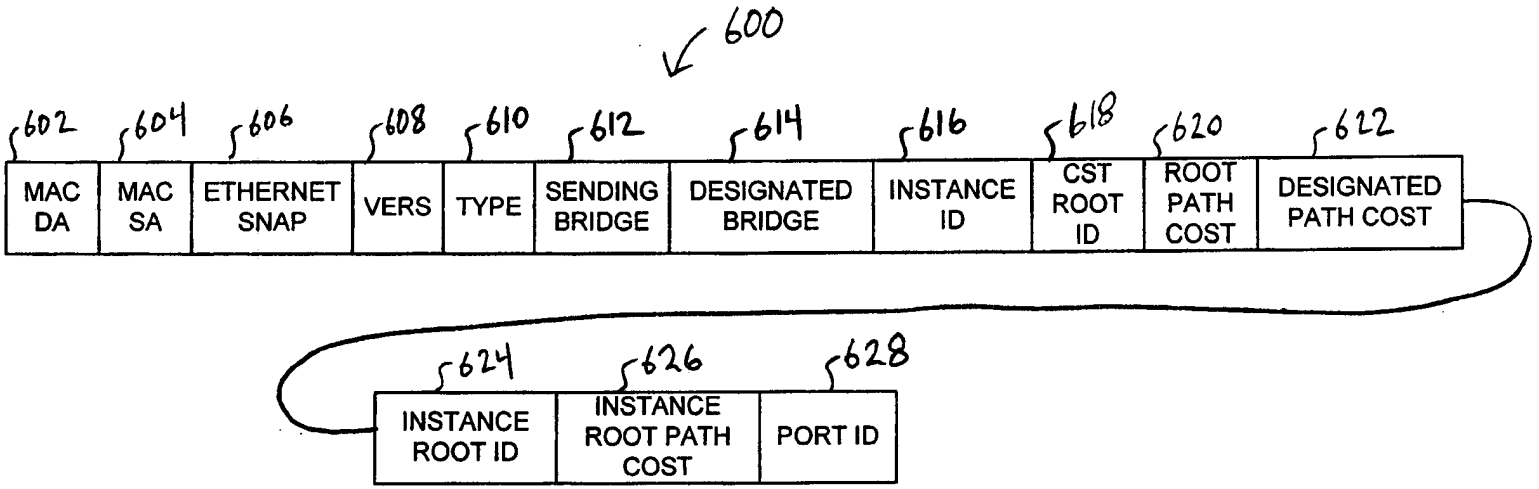
600
602
604
606
608
610
612
614
616
618
620
622
MAC DA
MAC SA
ETHERNET SNAP
VERS
TYPE
SENDING BRIDGE
DESIGNATED BRIDGE
INSTANCE ID
CST ROOT ID
ROOT PATH COST
DESIGNATED PATH COST
624
626
628
INSTANCE ROOT ID
INSTANCE ROOT PATH COST
PORT ID

FIG. 6